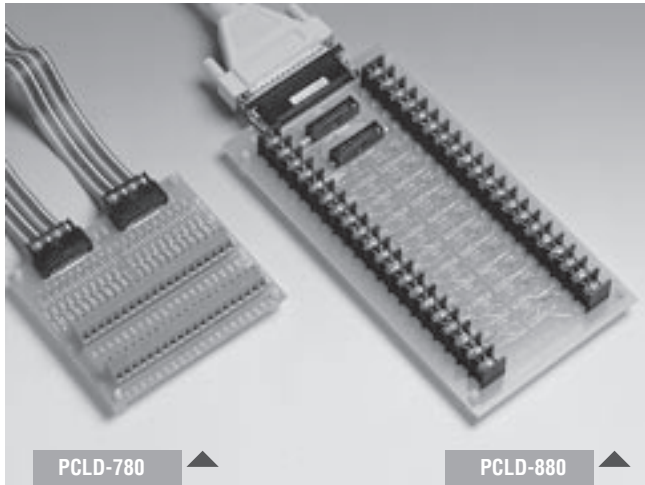


PCLD-780 PCLD-880

Screw Terminal Board Industrial Wiring Terminal Board w/Adapter



Features

- Pin to pin design
- Low-cost universal screw-terminal boards for industrial applications
- 40 terminal points for two 20-pin flat cable connector ports
- Reserved space for signal-conditioning circuits such as low-pass filter, voltage attenuator and current-to-voltage conversion
- Table-top mounting using nylon standoffs. Screws and washers provided for panel or wall mounting
- PCLD-780 Only**
 - Screw-clamp terminal-blocks allow easy and reliable connections
 - Dimensions: 102 x 114 mm (4.0" x 4.5")
- PCLD-880 Only**
 - Supports PC-LabCard™ products with DB-37 connectors
 - Industrial-grade terminal blocks (barrier-strip) permit heavy-duty and reliable connections
 - Dimensions: 221 x 115 mm (8.7" x 4.5")

Introduction

PCLD-780 and PCLD-880 universal screw-terminal boards provide convenient and reliable signal wiring for PC-LabCard™ products with 20-pin flat-cable connectors. PCLD-880 is also equipped with a DB37 connector to support PC-LabCard™ products with DB37 connectors.

PCLD-780 and PCLD-880 let you install passive components on the special PCB layout to construct your own signal-conditioning circuits.

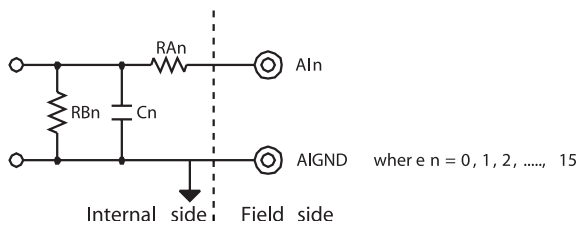
You can easily construct a low-pass filter, attenuator or current-to-voltage converter by adding resistors and capacitors onto the board's circuit pads.

Applications

- Field wiring for analog and digital I/O channels of PC-LabCard™ products which employ the standard 20-pin flat cable connectors or DB37 connectors (only PCLD-880)
- Signal conditioning circuits can be implemented as illustrated in the following examples:

a) Straight-through connection (factory setting)

$R_{An} = 0\Omega$ jumper



$R_{Bn} = \text{none}$

$C_n = \text{none}$

b) 1.6 kHz (3dB) low pass filter

$R_{An} = 10\text{ K}\Omega$

$R_{Bn} = \text{none}$

$C_n = 0.01\mu\text{F}$

$$f_{3dB} = \frac{1}{2\pi R_{An} C_n}$$

c) 10 : 1 voltage attenuator:

$R_{An} = 9\text{ K}\Omega$

$R_{Bn} = 1\text{ K}\Omega$

$C_n = \text{none}$

$$\text{Attenuation} = \frac{R_{Bn}}{R_{An} + R_{Bn}}$$

(Assume source impedance $\ll 10\text{ K}\Omega$)

d) 4 ~ 20 mA to 1 ~ 5 VDC signal converter:

$R_{An} = 0\Omega$ (short)

$R_{Bn} = 250\Omega$ (0.1% precision resistor)

$C_n = \text{none}$

Pin Assignments

CN1				CN5 (PCLD-880 only)			
A1	1	2	A2	A1	1	20	A2
A3	3	4	A4	A3	2	21	A4
A5	5	6	A6	A5	3	22	A6
A7	7	8	A8	A7	4	23	A8
A9	9	10	A10	A9	5	24	A10
A11	11	12	A12	A11	6	25	A12
A13	13	14	A14	A13	7	26	A14
A15	15	16	A16	A15	8	27	A16
A17	17	18	A18	A17	9	28	A18
A19	19	20	A20	A19	10	29	A20
				B1	11	30	B2
CN2				B3	12	31	B4
B1	1	2	B2	B5	13	32	B6
B3	3	4	B4	B7	14	33	B8
B5	5	6	B6	B9	15	34	B10
B7	7	8	B8	B11	16	35	B12
B9	9	10	B10	B13	17	36	B14
B11	11	12	B12	B15	18	37	B16
B13	13	14	B14	B17	19		
B15	15	16	B16				
B17	17	18	B18				
B19	19	20	B20				

Ordering Information

- PCLD-780** Screw terminal Board, two 1m 20-pin flat cables (PCL-10120-1)
- PCLD-880** Industrial Wiring Terminal Board, two 1m 20-pin flat cables (PCL-10120-1), and one PCL-10501 adapter (20-pin analog flat connector to DB37 connector)
- PCL-10137-1** DB37 cable assembly, 1 m
- PCL-10137-2** DB37 cable assembly, 2 m
- PCL-10137-3** DB37 cable assembly, 3 m