AIMB-212

Intel® Atom™ N450/D510 Mini-ITX with VGA/LVDS, 6 COM, and Dual LAN



Features

- Supports Intel® Atom™ processor N450 and D510 dual core
- One 200-pin SODIMM up to 2 GB DDR2 667 MHz SDRAM
- Supports 1 PCI and 1 Mini-PCIe expansion, 6 serial ports, 8 USB, and CF
- Lower total cost of ownership with DC12V support
- Supports embedded software APIs and Utilities

Software APIs:





















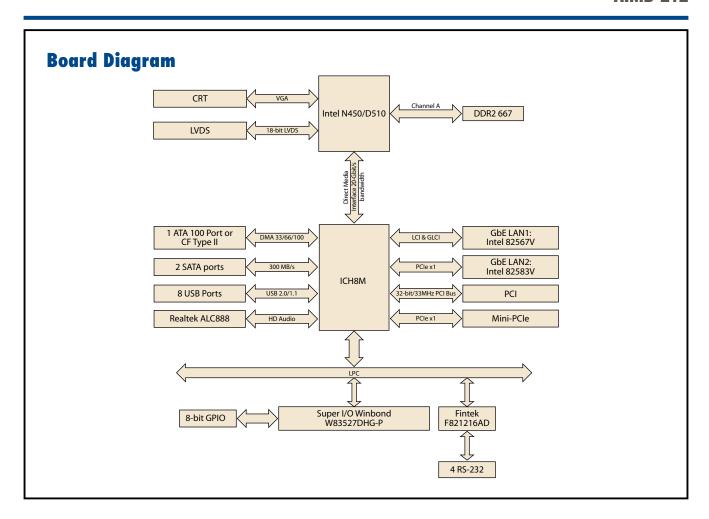




Note: eSOS need ODM BIOS by request

Specifications

| | CPU (45 nm) | Intel Atom N450 | Intel Atom D510 | | |
|-------------------------|------------------------|--------------------------------------|---|--|--|
| | Max. Speed | 1.66 GHz (single core) | 1.66 GHz (dual core) | | |
| Processor System | L2 Cache | 512 KB | 1 MB | | |
| 1 10003301 Oysterii | Chipset | ICH8M | TIVID | | |
| | BIOS | AMI 16 Mbit PSI | | | |
| | PCI | 32-bit/33 MHz, 1 slot | | | |
| Funancian Clat | | 32-DII/33 IVITZ, 1 SIUI | | | |
| Expansion Slot | Mini-PCle | | | | |
| | PCle | | CILL OF PROCESS AND | | |
| | Technology | Single channel DDR2 667 MHz not c | ompatible with DDK2533 MHZ | | |
| Memory | Max. Capacity | 2 GB | | | |
| | Socket | 1 x 200-pin SODIMM | | | |
| | Controller | | 00-MHz render clock frequency for N450/D510 | | |
| | VRAM | Shared system memory up to 224 MI | | | |
| Graphics | VGA | | 60Hz for Atom N450, up to 2048 x 1536 for Atom D510 | | |
| шаршо | LVDS | Supports 18-bit single channel and u | p to WXGA 1366 x 768 | | |
| | TV-out | None | | | |
| | Dual Display | CRT + LVDS, support extended mode | and clone mode | | |
| | Interface | 10/100/1000 Mbps | | | |
| Ethernet | Controller | GbE LAN1: Intel 82567V; GbE LAN2: | Intel 82583V | | |
| | Connector | RJ-45 x 2 | | | |
| | Max Data Transfer Rate | 300 MB/s | | | |
| SATA | Channel | 2 | | | |
| | Mode | None | | | |
| EIDE | Channel | None | | | |
| SSD | CompactFlash | Supports CompactFlash Type I/II | | | |
| 000 | VGA | 1 | | | |
| | Ethernet | 2 | | | |
| | USB | 4 (USB 2.0 compliant) | | | |
| Door I/O | | 3 (Mic-in, Line-out, Line-in) | | | |
| Rear I/O | Audio | | | | |
| | Serial | 3 (2 of RS-232,1 of RS-232/422/485 | | | |
| | Parallel | - | | | |
| | DC jack | 1 (2.5 mm) | | | |
| | LVDS & Inverter | 1 | | | |
| | USB | 4 (USB 2.0 compliant) | | | |
| | Serial | 3 (RS-232) | | | |
| | IDE | None | | | |
| Internal Connector | SATA | 2 | | | |
| | SATA PWR connector | 2 | | | |
| | CompactFlash | 1 | | | |
| | Parallel | None | | | |
| | DIO | 8-bit GPIO | | | |
| Watah dan Timon | Output | System reset | | | |
| Watchdog Timer | Interval | Programmable 1 ~ 255 sec/min | | | |
| Dower Paguiromente | | DC 12V Input (Tolerance ±10%) | | | |
| Power negurierieris | Typical | | | | |
| Power Requirements | Typical | | Non-Operating | | |
| Environment Environment | Temperature | Operating 0 ~ 60° C (32 ~ 140° F) | Non-Operating -40 ~ 85° C (-40 ~ 185° F) | | |



Ordering Information

| Part Number | CPU | SC/DC | GbE | COM | LVDS |
|-----------------|-----------|-------------|-----|-----|-----------|
| AIMB-212N-S6A1E | Atom N450 | Single core | 2 | 6 | 1, 18-bit |
| AIMB-212D-S6A1E | Atom D510 | Dual core | 2 | 6 | 1, 18-bit |

Packing List

| Part number | Description | Quantity |
|----------------|---------------------------------|----------|
| 1700003194 | SATA HDD cable | 2 |
| 1700017461 | SATA power cable | 2 |
| 1700001788 | Serial port cable | 3 |
| 1960046526N001 | CPU cooler (For Atom D510 only) | 1 |
| 1960046435T000 | I/O port bracket | 1 |
| 2002021210 | Startup manual | 1 |
| 2062021200 | Driver CD | 1 |

Optional Accessories

| Part Number | Description |
|-------------|------------------------------------|
| 1700003195 | USB cable with four ports, 17.5 cm |
| 1700002204 | USB cable with four ports, 27 cm |
| 1700008461 | USB cable with four ports, 30.5 cm |
| 1757003082 | Adapter AC100-240V 60W +12V/5A |

Embedded OS/API

| Embedded OS/API | Part No. | Description |
|-----------------|------------|---|
| Win XPE | 2070009030 | XPE WES2009 AIMB-210 V4.0 ENG |
| | 2070009031 | XPE WES2009 AIMB-210 V4.0 24MUI |
| Software API | 205E212000 | SUSI 3.0 SW API for AIMB-212 B:20091115 XP |

Bracket View



Value-Added Software Services

Software API: An interface that defines the ways by which an application program may request services from libraries and/or operating systems. Provides not only the underlying drivers required but also a rich set of user-friendly, intelligent and integrated interfaces, which speeds development, enhances security and offers add-on value for Advantech platforms. It plays the role of catalyst between developer and solution, and makes Advantech embedded platforms easier and simpler to adopt and operate with customer applications.

Software APIs

Control



General Purpose Input/Output is a flexible parallel interface that allows a variety of custom connections. It allows users to monitor the level of signal input or set the output status to switch on/off a device. Our API also provides Programmable GPIO, which allows developers to dynamically set the GPIO input or output status.



SMBus is the System Management Bus defined by Intel® Corporation in 1995. It is used in personal computers and servers for low-speed system management communications. The SMBus API allows a developer to interface a embedded system environment and transfer serial messages using the SMBus protocols, allowing multiple simultaneous device control.



I²C is a bi-directional two wire bus that was developed by Philips for use in their televisions in the 1980s.

The I²C API allows a developer to interface with an embedded system environment and transfer serial messages using the I²C protocols, allowing multiple simultaneous device control.

Monitor



A watchdog timer (WDT) is a device that performs a specific operation after a certain period of time if something goes wrong and the system does not recover on its own.

A watchdog timer can be programmed to perform a warm boot (restarting the system) after a certain number of seconds.



The Hardware Monitor (HWM) API is a system health supervision API that inspects certain condition indexes, such as fan speed, temperature and voltage.



Control

Power Saving

Monitor

The Hardware Control API allows developers to set the PWM (Pulse Width Modulation) value to adjust fan speed or other devices; it can also be used to adjust the LCD brightness.

Display



Brightness Control The Brightness Control API allows a developer to interface with an embedded device to easily control brightness.



Make use of Intel SpeedStep technology to reduce power power consumption. The system will automatically adjust the CPU Speed depending on system loading.





System Throttling

Refers to a series of methods for reducing power consumption in computers by lowering the clock frequency. These APIs allow the user to lower the clock from 87.5% to 12.5%.

Software Utilities



BIOS Flash

The BIOS Flash utility allows customers to update the flash ROM BIOS version, or use it to back up current BIOS by copying it from the flash chip to a file on customers' disk. The BIOS Flash utility also provides a command line version and API for fast implementation into customized applications.



Embedded Security ID

The embedded application is the most important property of a system integrator. It contains valuable intellectual property, design knowledge and innovation, but it is easily copied! The Embedded Security ID utility provides reliable security functions for customers to secure their application data within embedded RIOS



The Monitoring utility allows the customer to monitor system health, including voltage, CPU and system temperature and fan speed. These items are important to a device; if critical errors happen and are not solved immediately, permanent damage may be caused.



eSOS

The eSOS is a small OS stored in BIOS ROM. It will boot up in case of a main OS crash. It will diagnose the hardware status, and then send an e-mail to a designated administrator. The eSOS also provides remote connection: Telnet server and FTP server, allowing the administrator to rescue the system.



Flash Lock

Flash Lock is a mechanism that binds the board and CF card (SQFlash) together. The user can "Lock" SQFlash via the Flash Lock function and "Unlock" it via BIOS while booting. A locked SQFlash cannot be read by any card reader or boot from other platforms without a BIOS with the "Unlock" feature.